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IDS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 09/920,978  
Filing Date ..... August 1, 2001  
Inventor ..... Shane J. Trapp  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... David S. Blum  
Attorney Docket No. .... MI22-1674  
Title: Method of Forming Integrated Circuitry and Method of Forming Shallow  
Trench Isolation in a Semiconductor Substrate

Shane J. Trapp  
8/18/03

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: \_\_\_\_\_

7/23/03

By: \_\_\_\_\_

Mark S. Matkin  
Reg. No. 32,268